





Department of Research and Development Department of Electrical and Electronics Engineering

Faculty Development Programme

Advancements in VLSI/Semiconductor Industry and Introduction to Design for Test Methodologies

- 22 March 2025
- © 09:30 AM 04:30 PM
- Tejas Seminar Hall



Praveen Kumar Reddy K
DFT Engineer - Qualcomm Iridia Pvt Ltd

Faculty Coordinators

Dr. Karthika M

Associate Professor - EEE

Dr. Gunapriya B

Associate Professor - EEE

Organizing Committee

Dr. Vinoth Kumar K

Professor and Associate Head - R&D Advisor - IEEE CSC NHCE SBC Dr. Sakthivel A

HoD-EEE

Dr. Revathi V

Dean R&D

Dr. Manjunatha

Principal











New Horizon College of Engineering, CSC41 (SBC66131H) Student Branch Chapter

Event Report

Title	Faculty Development Programme on "Advancements in VLSI /Semiconductor industry and Introduction to Design for Test Methodologies"				
Department	Research and Development & Electrical and Electronics Engineering				
Date	From: 22.03.2025	To: 22.03.2025			
Time	From: 09:30 AM	To: 04:30 PM			
Brief Description (4-5 Lines Max)	Development at New Horizon College of Enorganized a Faculty Development Programme Industry and Introduction to Design for Test M Tejas Seminar Hall from 09:30 AM to 04:30 PM researchers to enhance their knowledge in the technologies. The session was presented by Mr. at Qualcomm India Pvt Ltd, Bengaluru. The comprehensive understanding of the current add The program also focused on introducing facult Design for Test (DFT), a critical aspect of electronic systems. The event aimed at equipping the skills necessary to understand and apply contexts. The FDP provided ample opportunic Reddy. A dynamic Q&A session allowed the atopics related to VLSI and DFT. Participants of complex concepts, which helped further enhance Programme on "Advancements in VLSI/Semico Methodologies" was a resounding success. semiconductor landscape, enabling faculty meternds and methodologies. The program also for and industry, empowering the attendees with the practices in VLSI and related fields. The Depart Research and Development at New Horizon CCSE SBC, expressed their gratitude to Mr. delivering such an informative and engaging participants expressed their appreciation for contemporary topic. We would like to extend for his expert session and contribution to the organizing team from the Department of Electronic Standard Programment of Electronic Pr	om: 22.03.2025 To: 22.03.2025 om: 09:30 AM To: 04:30 PM 2025, the Department of Electrical and Electronics Engineering & Research and New Horizon College of Engineering, in collaboration with IEEE CSE SBC, alty Development Programme (FDP) on "Advancements in VLSI/Semiconductor roduction to Design for Test Methodologies." This insightful session was held at all from 09:30 AM to 04:30 PM, providing an opportunity for faculty members and hance their knowledge in the rapidly evolving field of VLSI and semiconductor essession was presented by Mr. Praveen Kumar Reddy K, a seasoned DFT Engineer India Pvt Ltd, Bengaluru. The primary goal of this FDP was to provide a understanding of the current advancements in the VLSI and semiconductor industry. To focused on introducing faculty and researchers to the essential methodologies of (DFT), a critical aspect of the design and manufacturing process in modern is. The event aimed at equipping participants with industry-relevant knowledge and harry to understand and apply these technologies in both academic and research DP provided ample opportunity for faculty and participants to interact with Mr. nic Q&A session allowed the attendees to gain a deeper understanding of specific VLSI and DFT. Participants were able to ask questions and seek clarification on st, which helped further enhance the value of the session. The Faculty Development Advancements in VLSI/Semiconductor Industry and Introduction to Design for Test was a resounding success. It provided valuable insights into the evolving andscape, enabling faculty members and researchers to stay updated on industry odologies. The program also fostered a sense of collaboration between academia powering the attendees with the knowledge to advance their research and teaching and related fields. The Department of Electrical and Electronics Engineering & evelopment at New Horizon College of Engineering, in collaboration with IEEE essed their gratitude to Mr. Praveen Kumar Reddy for his time and effort in an informative and enga			

















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Autonomous College Permanently Affiliated to VTU, Approved by AICTE & UGC Accredited by NBA and NAAC with Grade "A" New Horizon Knowledge Park, Ring Road, Bellandur Post, Bengaluru 560 103

Department of Research and Development Department of Electrical and Electronics Engineering Jointly Organizing

Faculty Development Program on Advancements in VLSI/Semiconductor Industry and Introduction to Design for Test Methodologies on 22nd March 2025

Participant List (Attendance)

S. No	Name	Department	Designation	Signature
1.	RAJ SINGH	EEE	Students	Roj
2.	Nishant hupta	EEE	Student	Nistant.
3	CHARAN-KORA	EEE	Student	Champ-
Н	Dhananjaya H	EEE	student	Ohonaging H
S	Thippeswamys	EEE	student	Thipperays
6	Rani Gupta R	566	student	*
7	Gowsi Manojkumar	666	student	Comi
g	IORA BASHIR	EEE	Student	Pena Bashir
9	yashaswin .B.S	EEE	Student	yashaswin B.S
10	Lalith Nanayan	EEE	Stewlent	Julis
#	govori Manojkumar	EEE	Student	
11	Isinivan Babu N	ELE	SAS	1
12	T.V.S. Adinaroya	FCE	SAP	and
13	Magendan R	ECB	SAP	Dery.
14	Napabhunnana. N	ME	SAP	Ma.
15	Dr Veeresha. G	ME	SAP	W.
16	Dr. Sudaeslan T.A	ME	AAZ	68
17	Pragati Tuupathi	ECE	SAP	Harpather
18	pr. Sampa Das	ECE	SAP	Sampa Daz
19	Sangertha - Can	EEE	Ap	80

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COMO.	Soungs K.V	ter	AP	Afr
24,	Dr. Sonali N.	CSE2	Asso. Pro	· Pro
25	Rajescania /	288	Foremen 7	
ac a	S.S. ddalingelwara	EEE	Lab Enstructor	Grand
27.	Venkatesha 3	EEE	hab Instructor	Welpay 9
28.	Rajesh . B.K	EEE	Lab Intrever	18
29.	Nara 8imha Murthy. N.	SEEE	Lab Trytorustor	Bung
30-	Dr. M. Karthibe	EBE	Associate prof	duthide
31.	Dr. B. Gunopija	EEE	Assa por	luri.
32.	Ch-karinen	565	STAP	ch.y
33	Dr. D. D. Padma Parya	Maths-AS	Sv. Asst. Prof	(25)
34.	Dr. K. Goppe	MECH	Assp. prof	le Hat
35	Ms. Naudini, T	NHCK	Sv. Asst. Prof	half.
36	Mv. Kvishnomuthy	NHCK	ANST Prof	The state of the s
37	Mr Manisha M	NHCK	Asst Proof	nool
38	BY. S. SUZITHA	NHCE	profession	Durulle
39.	Dingkara K.L	EEE	Student	Rind
40	Chethan.s	EEE	Student	Chethan &
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	Charles To Annual Control			
Marie .				